

[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

Search: The ACM Digital Library The Guide

Search Results for translat* high level into target and virtual machine

 [Feedback](#) [Report a problem](#) [Satisfaction survey](#)

Terms used

[translat](#) [high level](#) [into target](#) and [virtual machine](#)

Found 81,229 of 141,680


[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#) [more »](#)

[Advanced Search](#)
[Preferences](#)

Web Results 1 - 10 of about 11,400 for translating high level into virtual machine processors. (0.52 second)

Computer Basics

... machines can understand, whereas a **high-level** programming language is ... not make him busy with **translating** the Java ... packages are then divided **into** many categories ...
www.geocities.com/tyen2/tutorial/cbpta6.htm - 13k -
[Cached](#) - [Similar pages](#)

Sponsored Links

Translating Equipment
 Nationwide sales and rentals
 Wireless FM and IR equipment
www.LexiconUSA.com

[PPT] Chapter 1

File Format: Microsoft Powerpoint 97 - [View as HTML](#)
 ... 10. **High-Level** Language. **Level** 5. ... 15. Digital Logic. **Level** 0. CPU, constructed from digital logic gates. ... Binary Numbers. **Translating** between binary and decimal. ...
bsp.csie.nctu.edu.tw/courses/asm-sp/slide/alsp01.ppt - [Similar pages](#)

Translating/Interpreting

70 Foreign Languages / Localization
 Sign Language Interpreting, ASL
www.LanguageConnections.com

[See your message here...](#)

CommsDesign - NPU software taps virtual machine approach

... one could devise an extremely **high-level** functional (as ... or session, removing an outer header, **translating** IP addresses ... written in the language **into** the **virtual** ...
www.commsdesign.com/design_corner/showArticle.jhtml?articleID=17602639 - 46k -
[Cached](#) - [Similar pages](#)

[PDF] Behavior Processors: Layers between End-Users and Java Virtual ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)
 ... Commands Type Interactors Java low **high high** Figure 3 ... processing, Java is at the **level** that Postscript is in ... **Translating** agent rules directly **into** Java bytecode ...
www.cs.colorado.edu/~l3d/systems/agentsheets/Documentation/VL-97-Paper.pdf -
[Similar pages](#)

The Jikes Research Virtual Machine (RVM)

... The optimizing compiler begins by **translating** Java bytecodes to ... **Low-level** optimization After **high-level** analyses and ... to a **low-level** intermediate representation ...
www-106.ibm.com/developerworks/java/library/j-jalapeno/ - 101k - [Cached](#) - [Similar pages](#)

ITworld.com - Compilers, Interpreters and Bytecode

... With bytecode, a **high-level** program is translated **into** an ... a JIT compiler translates code **into** the host ... aims to boost performance by **translating** the most ...
www.itworld.com/AppDev/400/CWD01/2201Compilers/ - 44k - Sep 6, 2004 -
[Cached](#) - [Similar pages](#)

1 - 22 - 01

7 VMPP: a virtual machine for parallel processing

Loyot, E.C., Jr.; Grimshaw, A.S.;

Parallel Processing Symposium, 1993., Proceedings of Seventh International , 13-16 April 1993

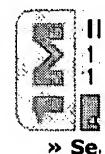
Pages:735 - 740

[\[Abstract\]](#) [\[PDF Full-Text \(476 KB\)\]](#) [IEEE CNF](#)

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)

IEEE Xplore®

RELEASE 1.8

Welcome
United States Patent and Trademark Office[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)**Quick Links**

Welcome to IEEE Xplore®

- [Home](#)
- [What Can I Access?](#)
- [Log-out](#)

Tables of Contents

- [Journals & Magazines](#)
- [Conference Proceedings](#)
- [Standards](#)

Search

- [By Author](#)
- [Basic](#)
- [Advanced](#)

Member Services

- [Join IEEE](#)
- [Establish IEEE Web Account](#)
- [Access the IEEE Member Digital Library](#)

IEEE Enterprise

- [Access the IEEE Enterprise File Cabinet](#)

[Print Format](#)Your search matched **7 of 1067317** documents.A maximum of **500** results are displayed, **50** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or enter a new one in the text box.

 Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard

5 Design of a Machine-Independent Optimizing System for Emulator Development

Perng-Ti Ma, T. G. Lewis

April 1980 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 2 Issue 2

Full text available:  [pdf\(1.32 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Methods are described to translate a certain machine-independent intermediate language (IML) to efficient microprograms for a class of horizontal microprogrammable machines. The IML is compiled directly from a high-level microprogramming language used to implement a virtual instruction set processor as a microprogram. The primary objective of the IML-to-host machine interface design is to facilitate language portability. Transportability is accomplished by use of a field descriptor ...

Machine-adaptable dynamic binary translation

David Ung, Cristina Cifuentes

January 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN workshop on Dynamic and adaptive compilation and optimization,**
Volume 35 Issue 7

Full text available: [pdf\(1.23 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#),
[index terms](#)

Dynamic binary translation is the process of translating and optimizing executable code for one machine to another at runtime, while the program is "executing" on the target machine.

Dynamic translation techniques have normally been limited to two particular machines; a competitor's machine and the hardware manufacturer's machine. This research provides for a more general framework for dynamic translations, by providing a framework based on specifications of machines that ...

Keywords: binary translation, dynamic compilation, dynamic execution, emulation, interpretation